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ABSTRACT

Properties of minority carrier (electron) traps in Schottky type p-GaN gate high electron mobility transistors were explicitly investigated by optical deep level transient spectroscopy (ODLTS). By temperature-scanning ODLTS, three electron traps, namely, E1, E2, and E3, were revealed, together with activation energy, capture cross section, and trap concentration. A thermally accelerated electron-releasing process of traps was quantitatively studied by Laplace ODLTS with individual emission time constant disclosed. At 300 K, the emission time constant was determined to be 0.21 and 1.40 s for E2 and E3, respectively, which adjacently existed in the bandgap and held activation energies of over 0.6 eV. As varying the optical injection pulse duration, a three-dimensional mapping of capacitance transient was obtained for each trap, attesting to the electron capture capability of each trap. By varying the reverse bias, the analysis of the ODLTS signal amplitude indicates that all three electron traps are located inside the p-GaN layer rather than the surface defect related.

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AlGaN/GaN heterostructure-based high electron mobility transistors (HEMTs) are promising devices for high-power and high-frequency applications, owing to large electron mobility, superior critical electrical field, and outstanding thermal stability.^{1–3} Recently, enhancement-mode HEMTs with a *p*-GaN gate are receiving extensive research interest for the reasons of easy gate driving and fail-safe properties, in addition to small gate charge and low on-resistance. GaN power IC implemented by E/D-mode transistors and rectifiers has also been demonstrated recently based on a *p*-GaN gate HEMT platform.⁴

There have been some studies on the dynamic degradation of p-GaN gate HEMTs. Current collapse and threshold voltage instability have been reported upon various gate voltage stressing conditions.^{5–8} Positive threshold voltage shift,^{9,10} which may destructively impact the device on-resistance and the switching speed, has been observed after on-state gate bias stressing and is often linked to electron-trapping in the p-GaN layer.¹⁰ In addition, it is also pointed out that the emission

process of trapped electrons also exerts influence on the current collapse of p-GaN gate HEMTs.¹¹

Considering reliability issues of the *p*-GaN gate HEMTs, it is demanding to investigate deep-level traps in *p*-GaN, which are responsible for dynamic degradations. There have been some reports on the majority carrier (hole) traps in *p*-GaN layers by deep level transient spectroscopy (DLTS).¹²⁻¹⁴ A hole trap with an activation energy of 0.88 eV has been identified and regarded to be originated from carbon impurities (C_N).^{12,13} However, few studies have been devoted to minority carrier (electron) trap characteristics in the *p*-GaN layer, particularly in a *p*-GaN gate HEMT.^{15,16} As a matter of fact, minority carrier traps^{17,18} may reduce the minority carrier lifetime and electrical conductivity that are crucial to high-speed switching operations.¹⁹

Optical deep level transient spectroscopy (ODLTS)^{20,21} with optical stimulus for carrier generation is regarded as a powerful tool to investigate trap behaviors, particularly minority carrier trap properties. For example, electron and hole traps in *n*-GaN grown on different

substrates have been studied using 470 and 355 nm LEDs.²¹ Nitrogensite carbon in the *n*-GaN layer has been linked to a hole trap with the help of sub-bandgap-light irradiation.²² Existing reports show that it is quite challenging to distinguish adjacent multiple trap levels by conventional temperature-scanning DLTS.¹⁶ In order to precisely examine the broad DLTS signal spectrum and make adjacent trap levels distinguishable, advanced DLTS techniques, such as Laplace DLTS,^{23,24} could be adopted with electrical or optical stimulus.

In this work, properties of electron traps in the *p*-GaN layer of a *p*-GaN/AlGaN/GaN HEMT have been explicitly investigated by ODLTS techniques. By temperature-scanning ODLTS, three electron traps, namely, E1, E2, and E3 were revealed. Using high-resolution Laplace ODLTS, an individual trap energy level was well resolved. Moreover, temperature-dependent emission time constants of electron traps were also extracted by Laplace ODLTS. To profile the trap capture process and location in the device, the optical pulse duration and reverse bias were varied to manipulate the carrier trapping and de-trapping process. The investigation of minority carrier traps in the *p*-GaN layer via ODLTS provides supportive evidence for understanding the dynamic degradation in *p*-GaN gate HEMTs and gives impetus to further research on minority carrier traps in wide bandgap semiconductors.

The *p*-GaN HEMT used in this study consists of a 70 nm *p*-GaN layer, a 15 nm $Al_{0.3}Ga_{0.7}N$ barrier layer, and a thick GaN buffer grown on a Si substrate. To perform the ODLTS study, the epilayer was fabricated into Schottky metal/*p*-GaN/AlGaN/GaN diodes, as shown in Fig. 1(a). The Schottky contact was formed by depositing Ni/Au on the *p*-GaN, while Ti/Al/Ni/Au was deposited on the AlGaN layer, followed by thermal annealing to form Ohmic contacts.

Figure 1(b) shows temperature-dependent I-V characteristics. When the temperature was increased from 50 to 300 K, the turn-on voltage (using 1 A/cm² as the threshold criteria) was decreased from 1.71 to 1.52 V due to augmented thermionic emission of electrons flowing from a two-dimensional electron gas to a Schottky contact.



FIG. 1. (a) Schematic of a device structure for ODLTS measurements with a Schottky contact to p-type GaN. (b) I-V curves and (c) C-V curves.

From 50 to 300 K, the leakage current density is slightly increased from 0.30 to 0.88 mA/cm² at -10 V due to enhanced thermal generation of charge carriers in the depletion region. Figure 1(c) plots *C*-*V* characteristics at 50 and 300 K with a measurement frequency of 1 MHz. A relatively sharper transition in capacitance was observed as the temperature decreases. By defining the transition voltage to be the value of the applied voltage, the capacitance variation reached 50% of the total change. The transition voltage was barely shifted from 50 to 300 K due to relatively low Mg doping concentration (in the level of 10^{17} cm⁻³) in the *p*-GaN layer.

The temperature-scanning ODLTS spectra were applied to identify the existence and energy levels of traps in the *p*-type GaN layer, as shown in Fig. 2(a). Temperature-scanning ODLTS was conducted using an optical pulse whose duration t_{po} was set as 100 ms and a reverse bias U_R of -1 V. The optical pulse features a sub-bandgap wavelength of 405 nm, which supplies sufficient energy to activate electrons to be trapped by electron traps in the *p*-GaN layer. From the ODLTS spectrum, the negative ODLTS signals and relatively broad valley observed around 275 K indicated that multiple minority carrier traps existed in the *p*-GaN layer. In total, three electron traps were extracted by the Arrhenius relation

$$\ln(\tau T^2) = \frac{E_C - E_T}{kT} - \ln(\gamma \sigma_n), \qquad (1)$$



FIG. 2. (a) ODLTS spectrum by temperature-scanning at 1 MHz. (b) Arrhenius plots of three electron traps.

where *k* is the Boltzmann constant, E_T is the energy level of trap states, E_C is the bottom of the conduction band, σ_n is the capture cross section, τ is the emission time constant obtained by ODLTS methods, and γ is a constant related to the effective electron mass.²⁵

In addition to electron trap E1 observed around 190 K, two more traps E2 and E3 were extracted from the relative broad valley from 275 to 325 K. Figure 2(b) shows Arrhenius plots of three electron traps obtained from ODLTS spectra. The properties of traps, including the activation energy $(E_C - E_T)$, capture cross section (σ_n) , and trap concentration (N_T) , were summarized in Table I. The trap E1 with the activation energy of $0.29 \pm 0.03 \,\text{eV}$ has the smallest capture cross section and lowest N_T, showing its relatively weak electron capture capability than the other traps, in good agreement with relatively small ODLTS signal magnitude. E2 ($E_C - 0.69 \pm 0.04$ eV) and E3 $(E_C - 0.77 \pm 0.03 \text{ eV})$ shared similar activation energy, as shown by the overlapping ODLTS signals. Trap E3 has the highest concentration and largest activation energy among all the traps observed, indicating that E3 is the prominent trap level. Given the activation energy of trap E1, deep levels around $E_C - 0.3 \text{ eV}$ have also been observed in the n-GaN film with high dislocation density,²⁶ n-GaN on sapphire,²⁷ and irradiated n-GaN samples.²⁸ Levels near $E_C - 0.65 - 0.75$ eV have been reported elsewhere, e.g., in NH3-molecular beam epitaxy (MBE) and plasma-assisted MBE n-GaN layers, where N-rich growth has been promoted.²⁹ Levels at $E_C-0.7\pm0.05$ eV in n-GaN have been reportedly overlapped with some minor levels, forming a broad level in the bandgap.³⁰ Similar to the activation energy of E3, traps at $E_C - 0.78 - 0.81$ eV have been identified in n-GaN layers grown on SiC and Si substrates as well and have been regarded as dislocationrelated defects.2

Figure 3(a) shows representative Laplace ODLTS^{23,24} spectra of trap E1, measured at four different temperatures from 180 to 195 K. In the Laplace ODLTS analysis, the recorded capacitance transients were transformed into spectra by inverse Laplace transformation (ILT) employing Tikhonov regularization³¹ (for details see the supplementary material). From 180 to 195 K, only one signal peak could be observed in the Laplace ODLTS spectra. The peak location, which corresponds to the emission time constant, is shifting to the smaller emission time constant side as the temperature is increasing. Figure 3(b) illustrates the temperature-dependent emission time constants (τ_T) of E1 extracted by Laplace ODLTS spectra. As the temperature increases from 170 to 200 K, the τ_T of E1 is reduced from 3.50 to 0.23 s, indicating a thermally accelerated electron-releasing process for a filled minority carrier trap. The enhanced thermionic carrier emission agreed well with observations that DLTS peaks of the shallow minority carrier trap in p-type GaN occurred at higher temperatures for increased testing frequency.³² The phenomenon has been reported for hole emission from minority carrier traps of n-GaN with currentinjection by the pulsed forward voltage,^{22,33} while in this study the

TABLE I. Properties of electron traps extracted from temperature-scanning ODLTS.

Trap No.	$E_C - E_T$ (eV)	Capture cross section (cm ²)	$N_T imes 10^{15} \ ({ m cm}^{-3})$
E1	0.29 ± 0.03	2.94×10^{-17}	1.08
E2	0.69 ± 0.04	$5.61 imes10^{-14}$	1.65
E3	0.77 ± 0.03	2.57×10^{-13}	1.73



FIG. 3. (a) Representative Laplace ODLTS spectra of E1 from 180 to 195 K. (b) The temperature-dependent emission time constant (τ_T) of E1 extracted from Laplace ODLTS.

emission time constant was quantitatively extracted for minority carrier traps in the *p*-GaN layer with the help of sub-bandgap optical stimulus. Given the thermal emission time constants, the activation energy of E1 could be extracted around $E_C - 0.27$ eV in the Arrhenius plot according to Eq. (1). The activation energy was quite consistent with the results of temperature-scanning ODLTS. Trap E1 obtained in this study exhibits a relatively larger capture cross section than a similar electron trap ($E_C - 0.23$ eV) of a *p*-GaN HEMT, also revealed by DLTS measurements in the literature;⁵⁴ however, the value in our sample was limited to be as small as 2.94×10^{-17} cm², indicating E1 has a weaker capability of capturing electrons.

Figure 4(a) shows a representative capacitance transient at 300 K, in which E2 and E3 were involved. The complete transient can be well fitted by two single exponential transients with two different time constants. The extracted time constants were 0.21 and 1.40 s for E2 and E3, respectively. Laplace ODLTS was also employed to precisely distinguish individual contributions of adjacent traps, as shown in Fig. 4(b).

Two well-resolved signal peaks, representing electron traps E2 and E3 were obtained in Laplace ODLTS spectra at four fixed temperatures from 290 to 305 K. The magnitude of E3 is much larger than E2, indicating E3 is acting as a dominant trap over E2. Moreover, at elevating temperatures, both E2 and E3 show relatively smaller time constant as indicated by the peak location at different temperature steps. Figure 4(c) shows the extracted temperature-dependent emission time constants from Laplace ODLTS. From 290 to 305 K, the thermally enhanced emission time constants of E2 and E3 were



FIG. 4. (a) Transient curve fitted with two exponential components at 300 K. (b) Representative Laplace ODLTS spectra from 290 to 305 K. (c) The temperaturedependent τ_T of E2 and E3 traps extracted from Laplace ODLTS. Inset: τ_T in the logarithm scale as a function of temperature from 290 to 320 K. (d) Arrhenius plots of electron traps E2 and E3 extracted from Laplace ODLTS spectra.

shortened from 0.54 to 0.18 s and from 5.95 to 0.98 s, respectively. Due to the fact that the minority carrier emission process was accelerated as the temperature rises, an insignificant V_{th} shift and a fast recovery behavior would be expected for *p*-GaN gate HEMTs, justifying its capability of working properly at high temperatures.³⁵

Figure 4(d) shows the Arrhenius plot of electron traps E2 and E3 based on the time constants extracted from Laplace ODLTS spectra. The activation energy of E2 and E3 were well resolved and extracted to be 0.66±0.02 and 0.79±0.02 eV, respectively, in good agreement with trap levels extracted from temperature-scanning ODLTS (Fig. 2). Although a deep level similar to E2 has been reported before,³⁶ its emission time constant is still unknown. Laplace ODLTS managed to reveal the emission time constant of individual traps and their temperature dependence in this study. The trap concentration of E3 (N_T $= 1.08 \times 10^{15} \text{ cm}^{-3}$) is nearly one order of magnitude higher than an electron trap E_{p2} in *p*-GaN ($E_C - 0.79 \text{ eV}$, $N_T = 2.30 \times 10^{14} \text{ cm}^{-3}$), as reported in the literature.³⁶ In addition, trap E3 exhibits a larger capture cross section of 2.57×10^{-13} cm², compared with E_{n2} in p-GaN (σ_n of 5.9 × 10⁻¹⁴ cm²). The relatively large σ_n suggested that trap E3 is likely complexes or dislocation related.³⁶ A capture cross section at an order of 10^{-13} cm² has recently been reported for Shockley-Read- Hall (SRH)-type nonradiative recombination centers (NRCs) in Mg-doped GaN, which was believed to be originated from $V_{\text{Ga}}(V_{\text{N}})_{2-3}$.^{17,37} Based on the trap density and capture cross section, the minority carrier lifetime^{17,37} of E3 was calculated to be \sim 90 ps, indicating that the minority carrier capture process could be executed much faster than the corresponding minority carrier emission process

Figures 5(a)-5(c) illustrate 3D mappings of capacitance transient $\Delta C(t)$ for traps E1, E2, and E3, respectively. For each measurement, the optical pulse duration (t_{po}) was varied to fill the traps to a different extent. When the light injection was switched off, a capacitance transient as a result of the thermal emission of electrons from the filled traps was measured. The black dashed lines of each transient graph represent the projections of transient



FIG. 5. Normalized 3D mapping of ODLTS capacitance transients $\Delta C(t)$ as a function of the optical pulse duration (t_{po}) for (a) E1 (at 190 K), (b) E2 (at 280 K), and (c) E3 (at 300 K) with a fixed U_R of -1 V. (d) The maximum extent of ODLTS valleys $\Delta S(t_{po})$ of three electron levels as a function of t_{po} . (e) Variation of the maximum extent $\Delta S(U_R)$ in ODLTS spectra of three electron traps as a function of the reverse bias.

amplitudes. A larger optical pulse duration (t_{po}) could fill the traps to a higher level, as evidenced by the enhanced intensity of the capacitance transient when extending t_{po} . For each curve, the cease of $\Delta C(t)$ with time follows approximately mono-exponential dependence.

As shown in Fig. 5(d), increasing t_{po} makes the number of injected carriers to be captured as the traps increase,^{38,39} and thereby the maximum extent of ODLTS valleys $\Delta S(t_{po})$ enhances. The amplitude of $\Delta S(t_{po})$ is nearly saturated until t_{po} above 1 s. The $\Delta S(t_{po})$ of E2 and E3 are increasing in a faster rate than E1, which indicates the electron capture kinetics for E2 and E3 are larger than E1. In the graph, the slopes of E2 and E3 are close to each other, indicating that they hold the comparable capability of capturing electrons, as is consistent with the results in Table I.

It should be noted that the amplitude of ODLTS in Figs. 5(d) and 5(e) is proportional to the amplitude of transients in Figs. 5(a)–5(c), but the two values are not necessarily equal to each other. Figure 5(e) shows the ODLTS signal $\Delta S(U_R)$ as a function of the reverse bias. The amplitude of ODLTS is corresponding to the trap concentration. With low reverse bias ($U_R < -0.8$ V), the signals of traps were barely observed. The amplitude of ODLTS signals was increased when reverse bias becomes strengthened, indicating that the three electron traps are charged rather than being electrically neutral when being filled. The amplitude dependence on reverse bias suggested that the traps were not surface related⁴⁰ while located inside the *p*-GaN layer.^{41,42}

In summary, minority carrier (electron) traps in the p-GaN layer of Schottky type p-GaN HEMTs were studied by various ODLTS methods. Three electron traps were revealed by temperature-scanning ODLTS. Laplace ODLTS was employed to distinguish two neighboring traps (E2 and E3), which formed a relatively broad signal valley in temperature-scanning ODLTS. Temperature-dependent emission time constants of three trap levels including E1, E2, and E3 were investigated by Laplace ODLTS, showing the emission process was accelerated with increasing temperature. As varying the optical pulse durations, a 3D mapping of the capacitance transient graph was obtained for each deep-level trap. The carrier capture kinetic of three electron traps was studied, indicating all the traps are located inside the p-GaN layer rather than surface related. The extraction of minority carrier trap properties in the p-GaN layer supplies solid evidence for understanding the dynamic degradation of p-GaN gate HEMTs and provides a valuable guideline for further optimization of III-N devices with a *p*-GaN layer.

See the supplementary material for Laplace ODLTS principles to extract the trap parameters.

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AUTHOR DECLARATIONS

Conflict of Interest

The authors have no conflicts to disclose.

DATA AVAILABILITY

The data that support the findings of this study are available from the corresponding author upon reasonable request.

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